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FIG. 1

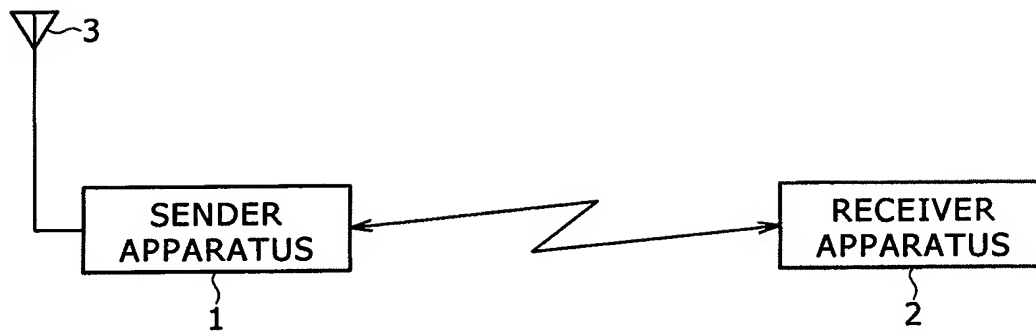


FIG. 2

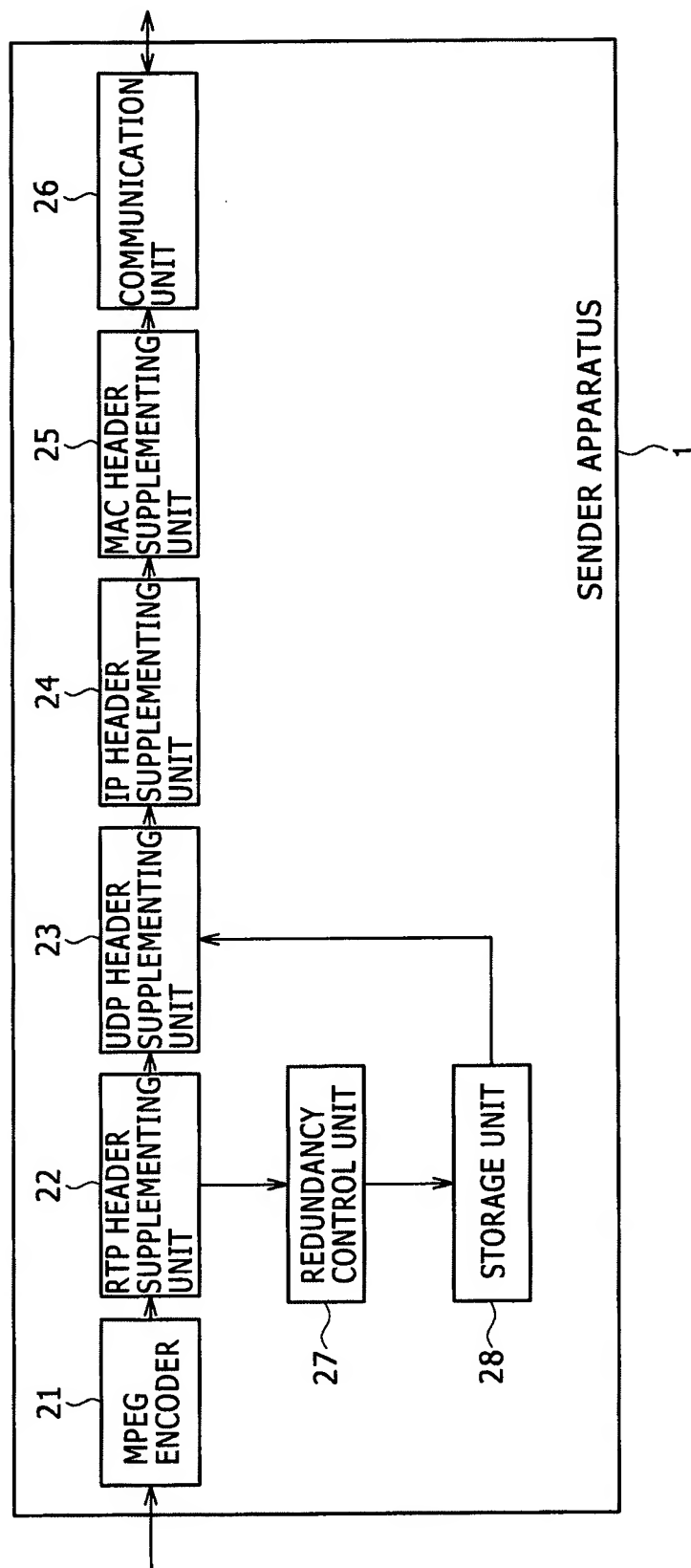


FIG. 3

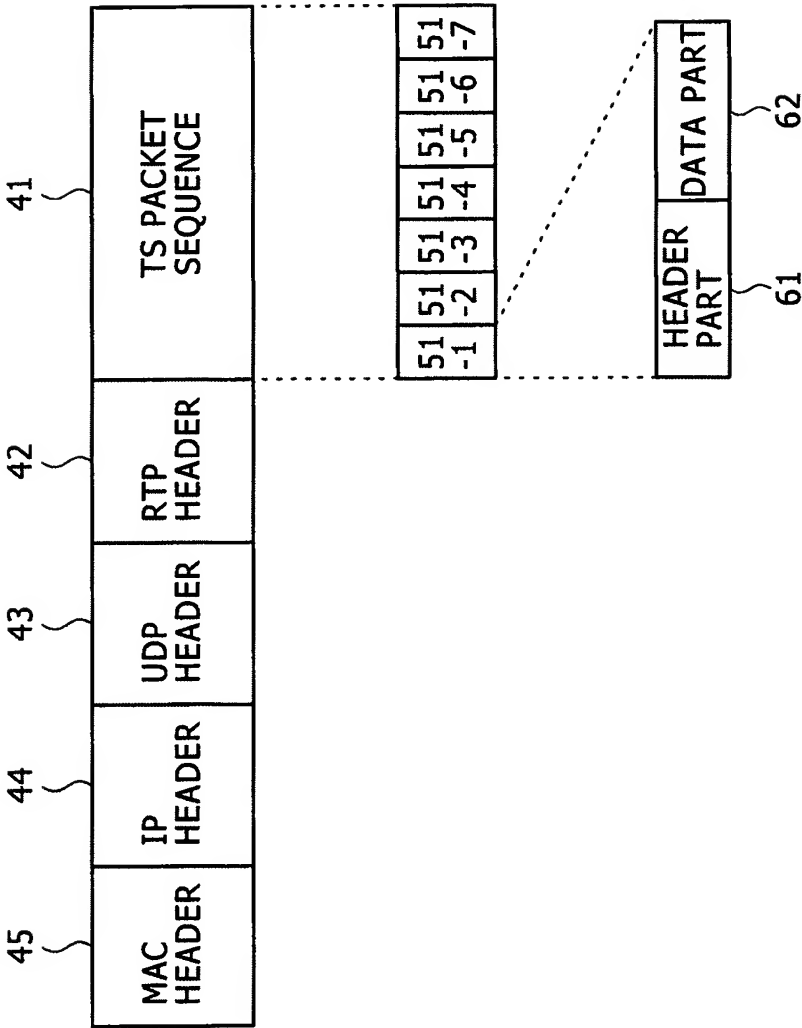


FIG. 4

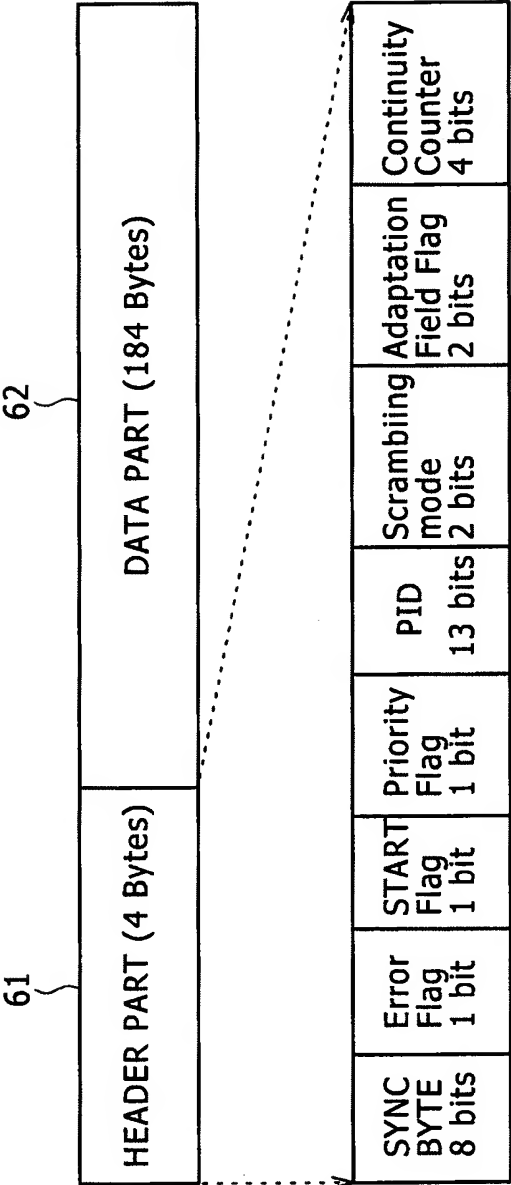


FIG. 5

V	P	X	CC	M	PT	Sequence Number
Time Stamp						
SSRC (Synchronization Source Identifier)						
CSRC (Contributing Source Identifiers)						

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FIG. 6

SRC PORT (16 bits)	DEST PORT (16 bits)
Length (16 bits)	Checksum (16 bits)

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FIG. 7

ver	IHL	TOS	TL	
ID		FO		
		FL	HC	
TTL	PROT	SA		
DA				

~ 44

FIG. 8

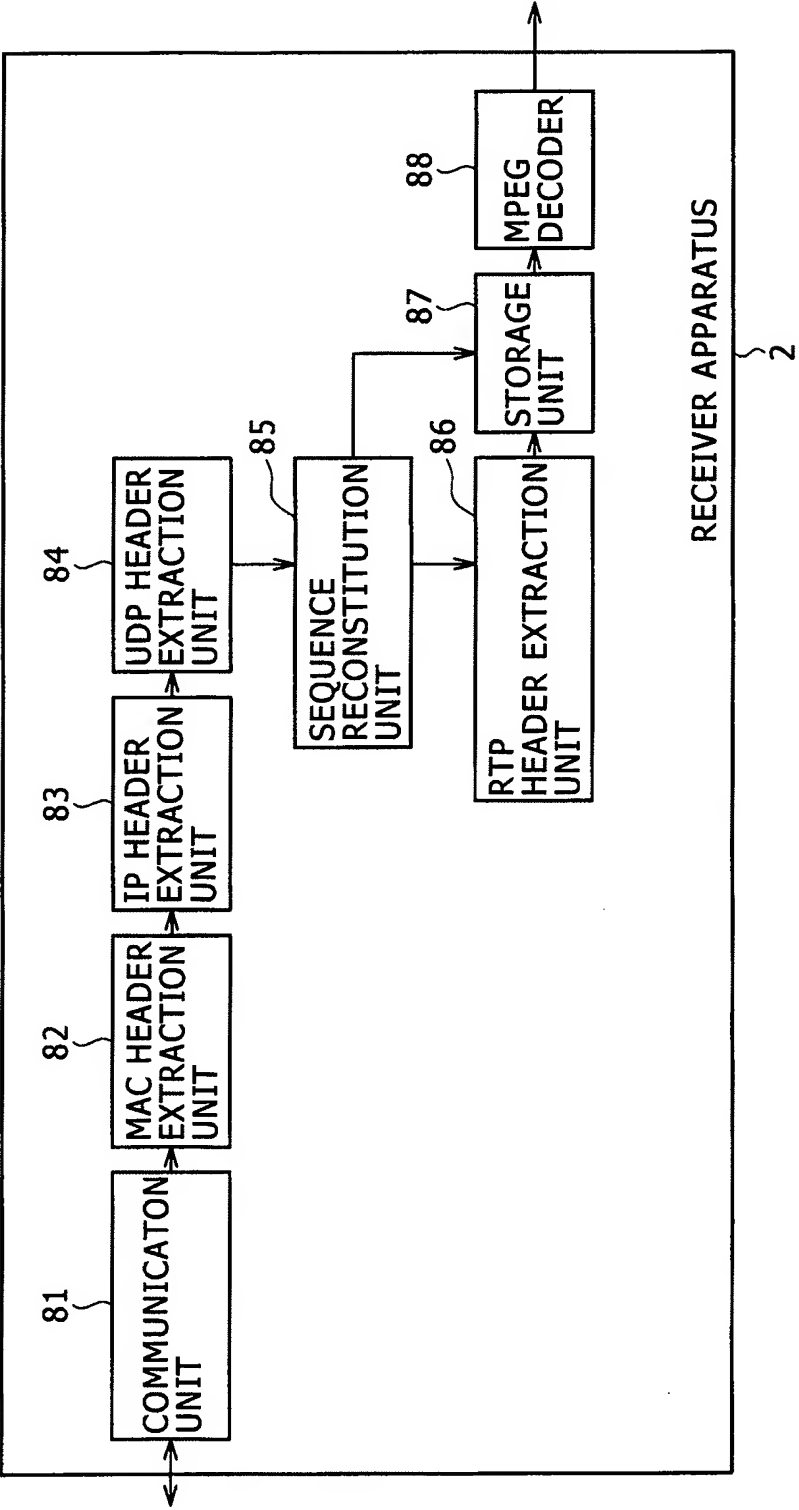
PA	DA	SA	<table><tr><td>Type</td></tr><tr><td>Length</td></tr></table>	Type	Length	FCS
Type						
Length						

~45

45



FIG. 9



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FIG. 10

SEQUESNCE NO.	
1	TS PACKET 51-1-1
	TS PACKET 51-2-1
	TS PACKET 51-7-1
2	TS PACKET 51-1-2
	TS PACKET 51-2-2
	TS PACKET 51-7-2
3	
4	TS PACKET 51-1-4
	TS PACKET 51-7-4

FIG. 11

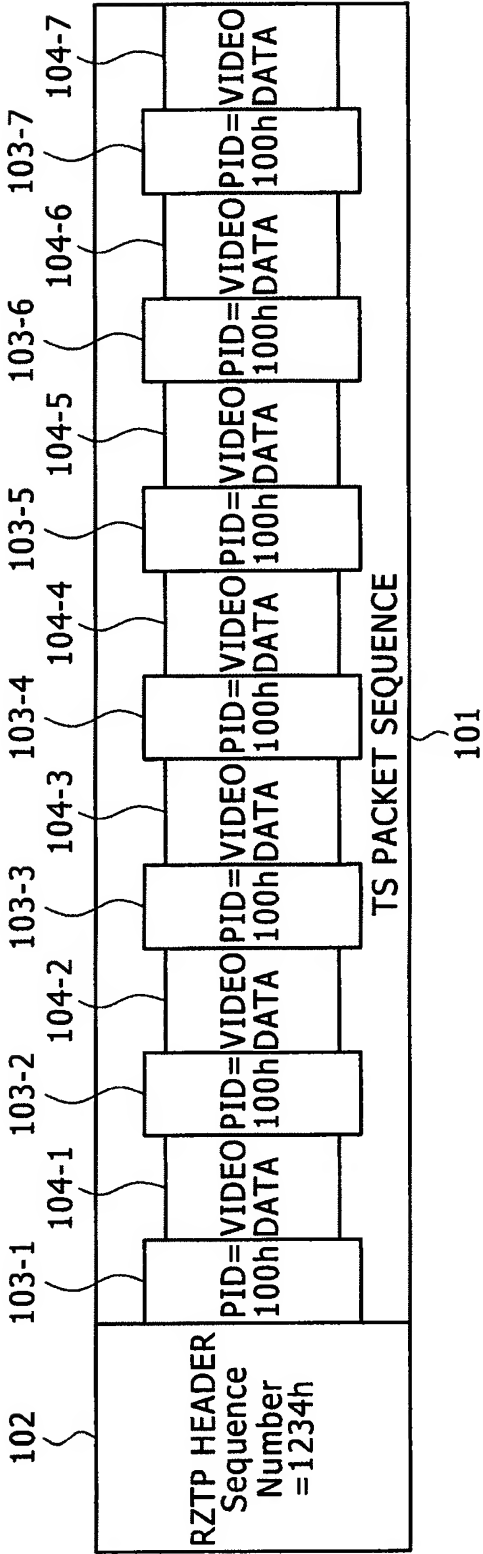
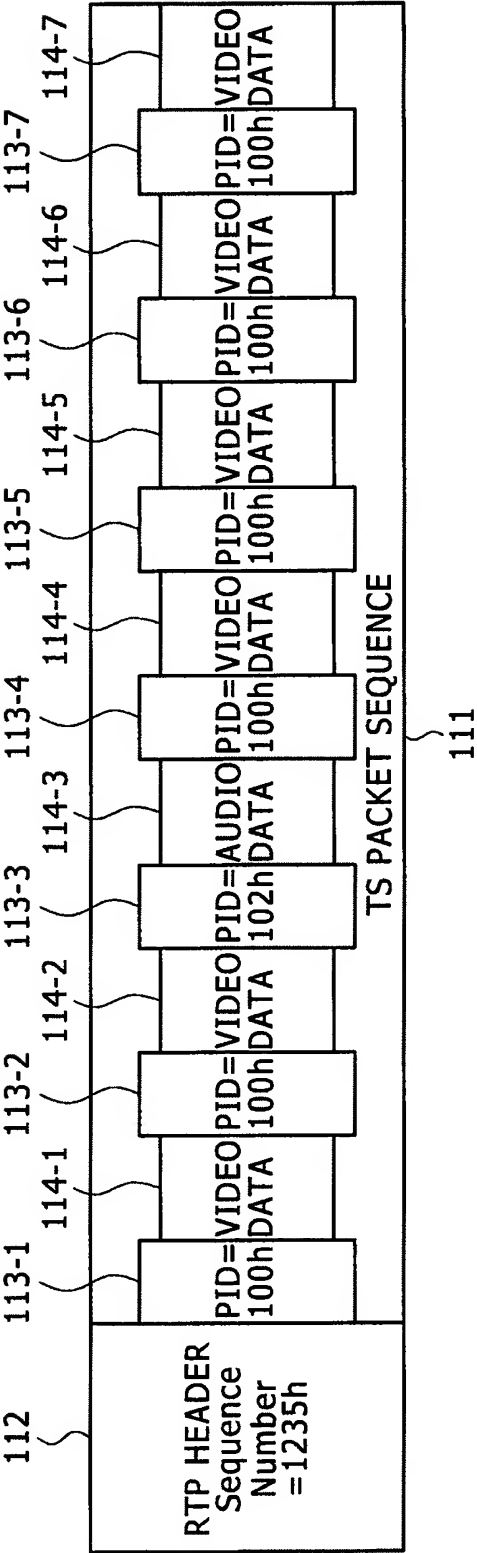


FIG. 12



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FIG. 13

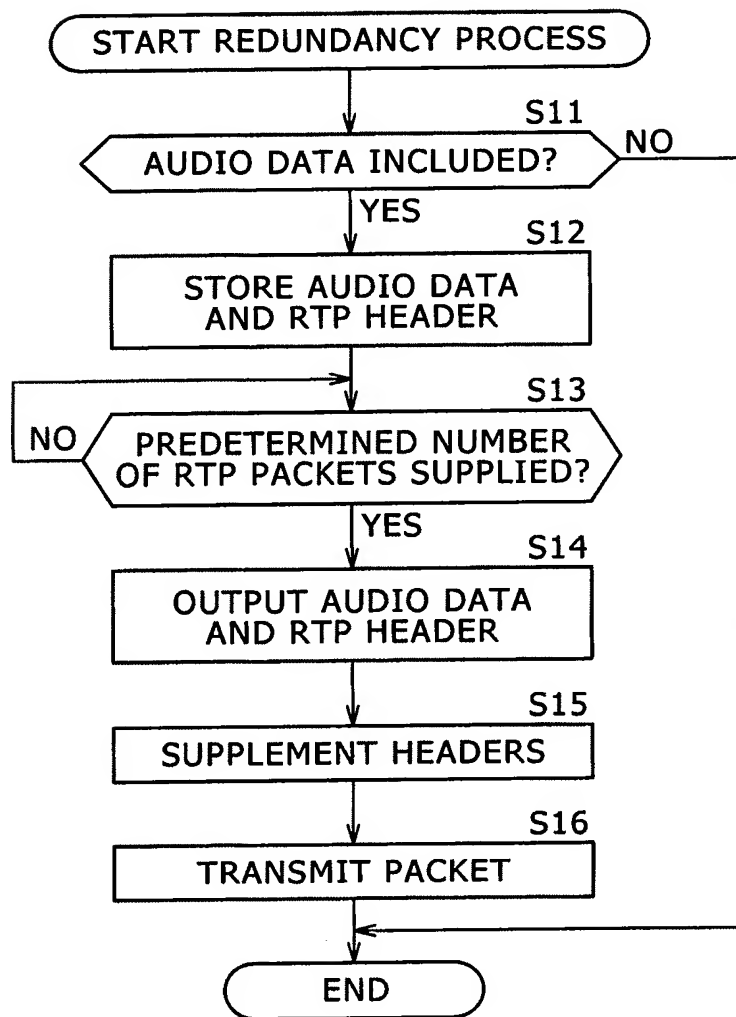
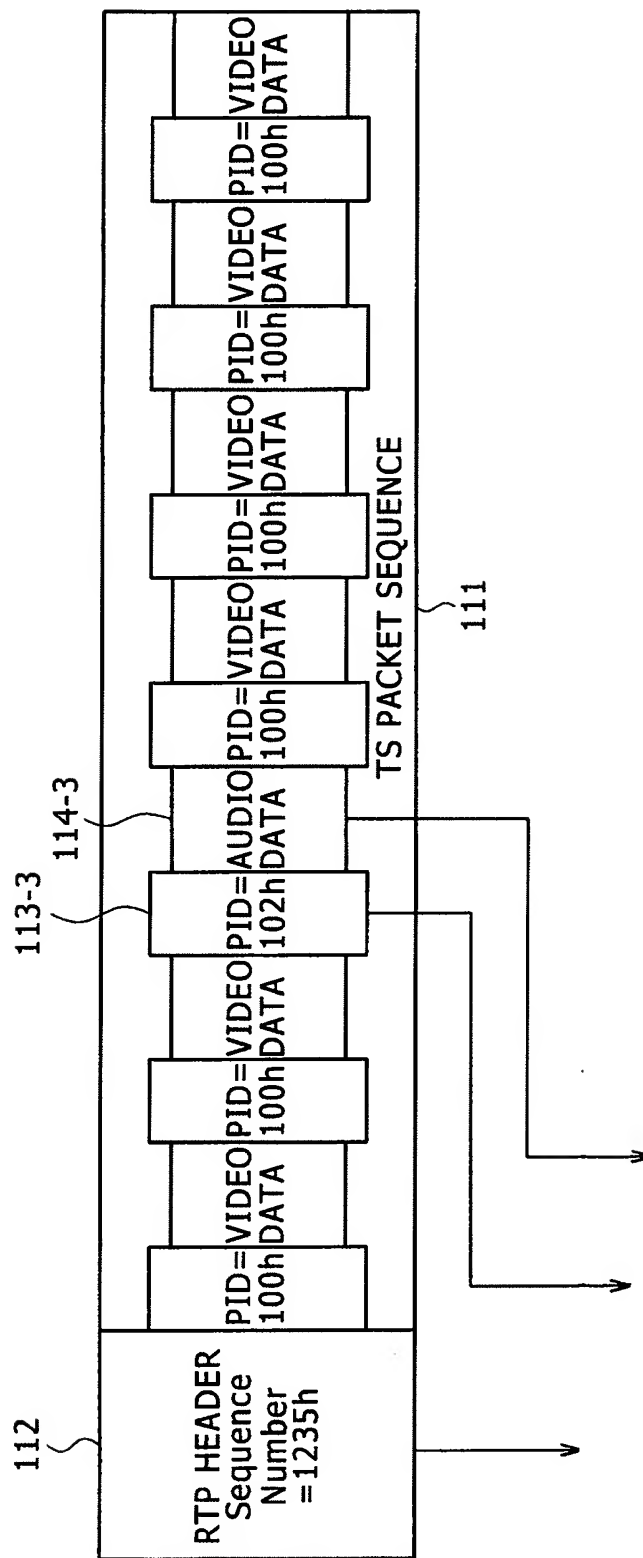


FIG. 14A



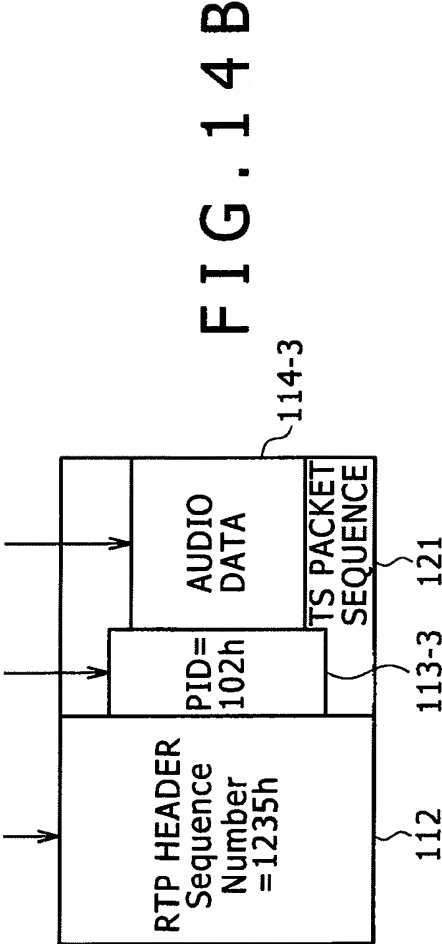


FIG. 15

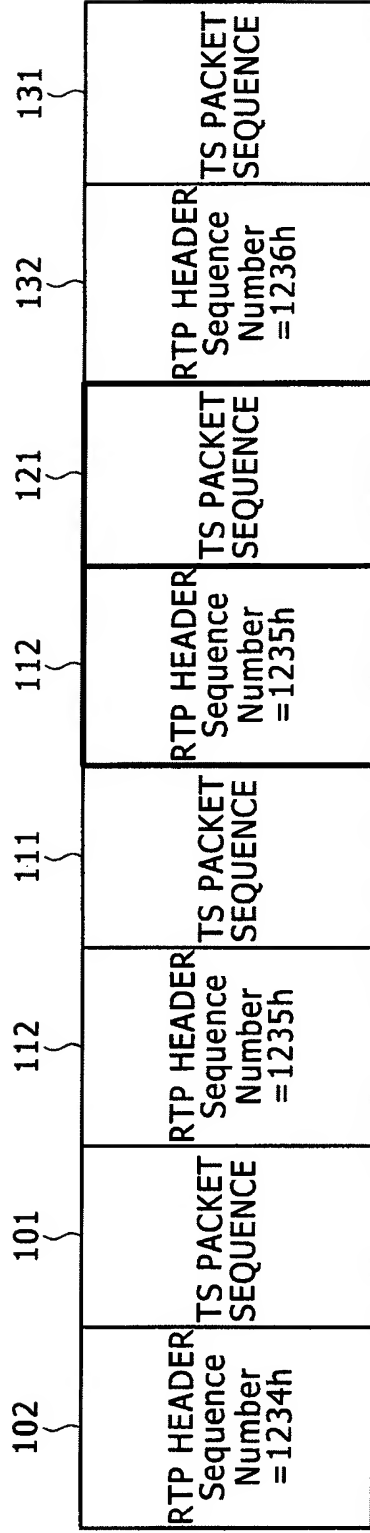
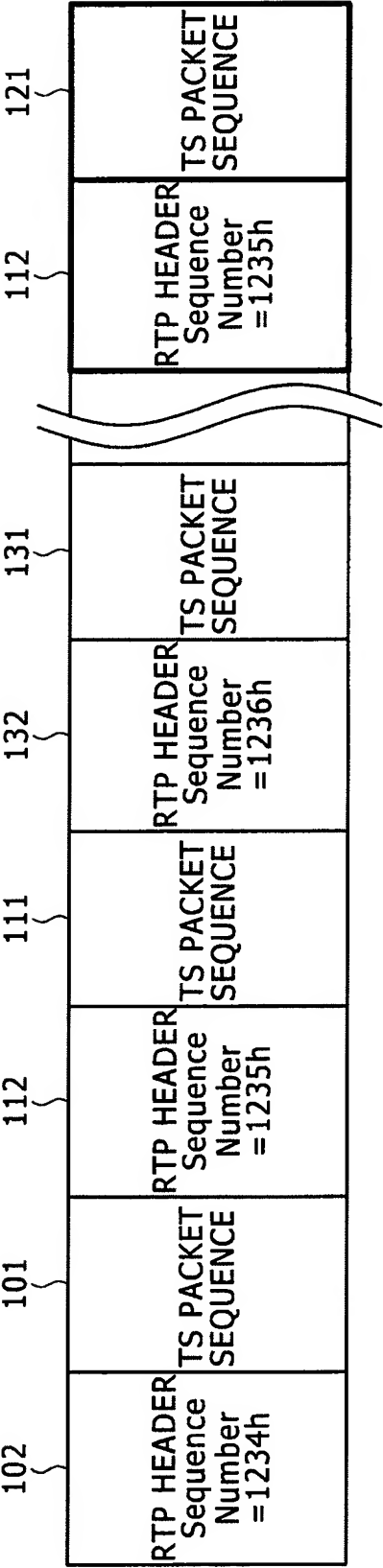




FIG. 16



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FIG. 17

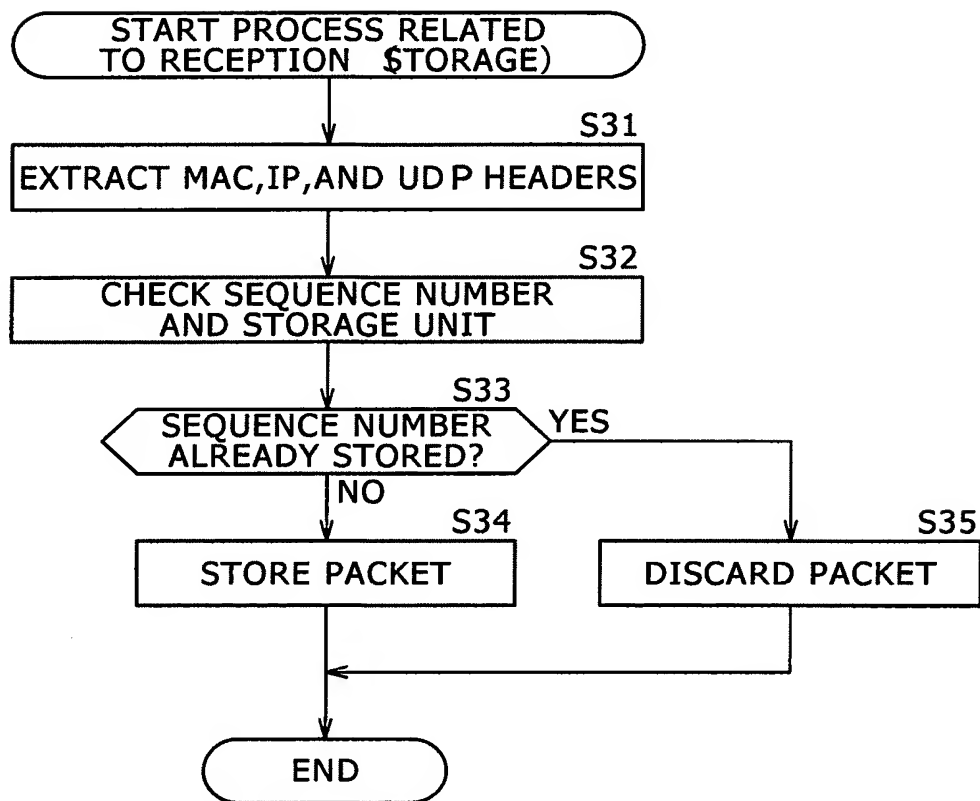


FIG. 18

